

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Bailey et al.

Docket No.: TI-27935

Serial No.: 09/452,691

Examiner: Fenty, J.

Filed: 12/02/99

Art Unit: 2815

FOR: AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED WITHIN A
MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER METAL
INTERCONNECT LAYER**REQUEST FOR EXTENSION OF TIME****FAX RECEIVED**Assistant Commissioner of Patents
Washington, DC 20231

APR 04 2003

TECHNOLOGY CENTER 2800

Dear Sir:

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner
for an extension of the shortened statutory period for response in the above-identified
Application.

The fee for this extension is indicated below:

04/10/2003 SSURLES 00000006 200668 09452691

01 FC:1251 110.00 CH ☒ One Month (\$110)☐ Two Months (\$410)☐ Three Months (\$930)☐ Four Months (\$1,450)


Please charge the fee to deposit account no. 20-0668. Any further necessary

04/11/2003 SSURLES 00000006 200668 09452691 extension of time is hereby requested. Charge any and all fees to deposit account no. 20-

01 FC:1252 300.00 CH 200668. An original and two copies of this sheet are enclosed.

Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265
(214) 532-9348
Fax: (972) 917-4418

Respectfully Submitted,


Jacqueline J. Garner
Reg. No. 36,144